

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	6536	710/305-307.ccls. or 710/309-310.ccls. or 710/313.ccls. or 710/315-316.ccls. or 710/52.ccls. or 710/62-64.ccls. 710/11.ccls. or 710/38.ccls. or 710/66.ccls. or 710/74.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 11:28
L3	6358	711/5.ccls. or 711/104-105.ccls. or 711/100-101.ccls. or 711/212.ccls. or 711/1.ccls. or 711/154.ccls. or 711/170.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 11:28
L4	21099	(microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 13:20
L5	14	sandorfi-miklos\$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 13:46
L6	192531	memory same (controller or interface or bridge) same (microprocessor or cpu or processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 13:47
L7	2079	((microprocessor or cpu or processor) near10 (interfac\$4 or bridge or adapter or adaptor)) same ((select\$3 or multiplex\$3) near15 (port or path))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 15:28
L8	131966	(microprocessor or cpu or processor) with (chip or asic or integrated or ic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 13:53
L9	37051	((interfac\$3 or bridge or controller) with (chip or asic or integrated)) same memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 13:56

L10	109	4 and 6 and 7 and 8 and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 13:57
L11	747	712/32.ccls. or 712/36.ccls. or 712/38.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 14:33
L12	1294	709/250.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 14:33
L13	4276	370/396.ccls. or 370/398.ccls. or 370/401-402.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 14:34
L14	18525	2 or 3 or 11 or 12 or 13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 14:35
L15	33	10 and 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 14:35
L16	27	15 and (@ad<="19990929" or @rlad<="19990929")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 15:28
L17	7	5 and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 15:28
L18	7	17 and (@ad<="19990929" or @rlad<="19990929")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 15:31

L19	4137	memory same (two with address\$4 with (portion or part))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 15:32
L20	2	10 and 19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 15:32